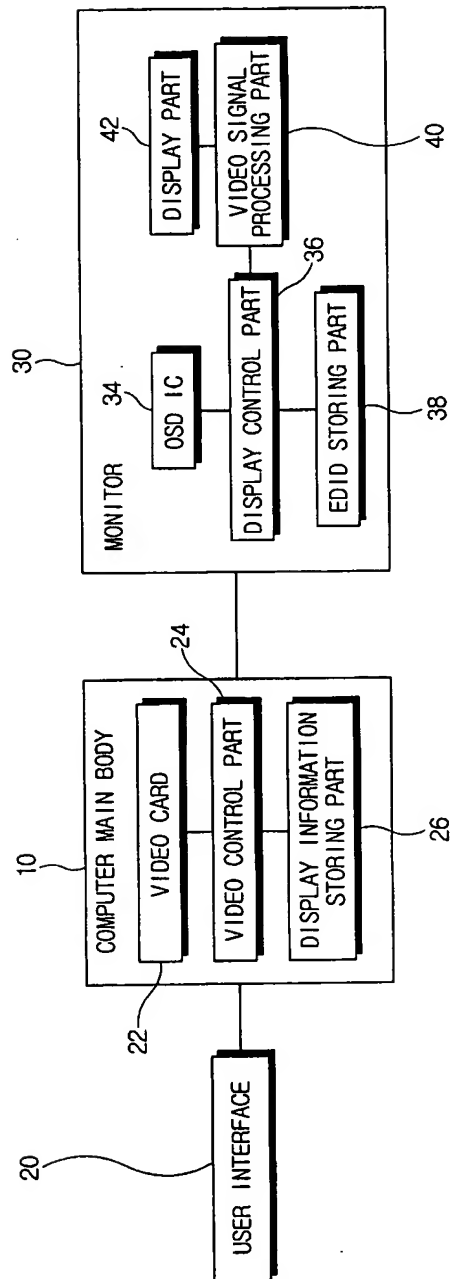
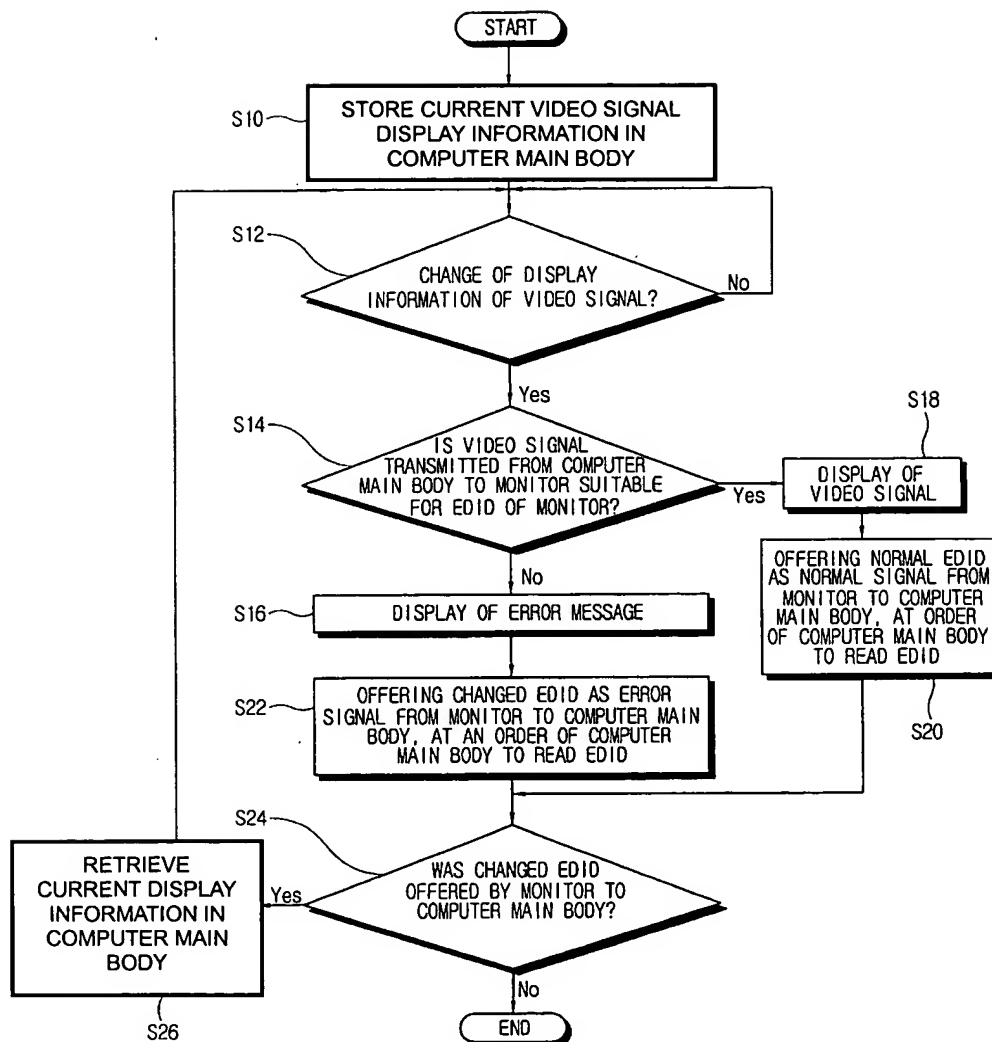


FIG. 1



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FIG. 2



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FIG. 3

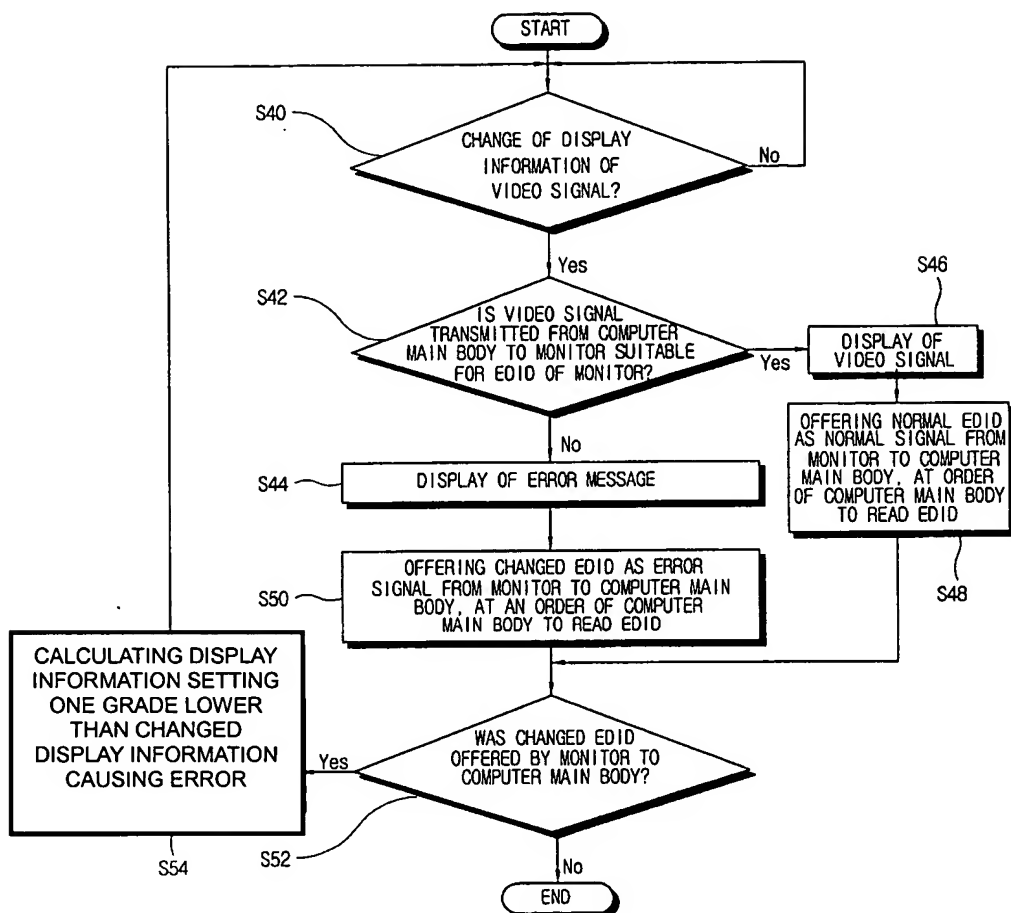


FIG. 4

VESA DDC EDID Version 2

Address	No Bytes	bit	CONTENTS	REMARK
00h	1	7 - 4	EDID Structure Version	
		3 - 0	EDID Structure Revision	
01h	2		ID Manufacturer Name TOTAL 15 BITS DIVIDED INTO EACH 5 BITS FORMING ABBREVIATION OF MANUFACTURER NAME TRANSFORMED TO CAPITALIZED LETTER	0 xxxxx xxxxx xxxxx
03h	2		ID Product Code	
05h	1		Week of Manufacture	
06h	2		Year of Manufacture	
08h	32		Manufacturer/Product ID String	
.	.		.	
.	.		.	

FIG. 5

Address	No Bytes	bit	CONTENTS	REMARK
00h	1	7 - 4	EDID Structure Version	
		3 - 0	EDID Structure Revision	
01h	2		ID Manufacturer Name TOTAL 15 BITS DIVIDED INTO EACH 5 BITS FORMING ABBREVIATION OF MANUFACTURER NAME TRANSFORMED TO CAPITALIZED LETTER	1 01111 01111 10010 _ (Out of Sync Range)
03h	2		ID Product Code	
05h	1		Week of Manufacture	
06h	2		Year of Manufacture	
08h	32		Manufacturer/Product ID String	
· ·	· ·		· ·	

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FIG. 6

